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ISSUE CLASSIFICATION	
Class	Subclass

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PATENT NUMBER

U.S. UTILITY Patent Application

to SCANNED	O.I.P.E. IX3 Q.A.	PATENT DATE
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APPLICATION NO. 09/851708	CONT/PRIOR D	CLASS 324	SUBCLASS 714 738	ART UNIT -2858	EXAMINER P. G. G. G.
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APPLICANTS

Indradeep Ghosh

TITLE

Automatic test pattern generation for functional register transfer level circuits using assignment decision diagrams

PTO-2040
12/99[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) _____ (Date) _____ (Primary Examiner) _____ (Date) _____ (Legal Instruments Examiner) _____ (Date)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ _____				ISSUE FEE	
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